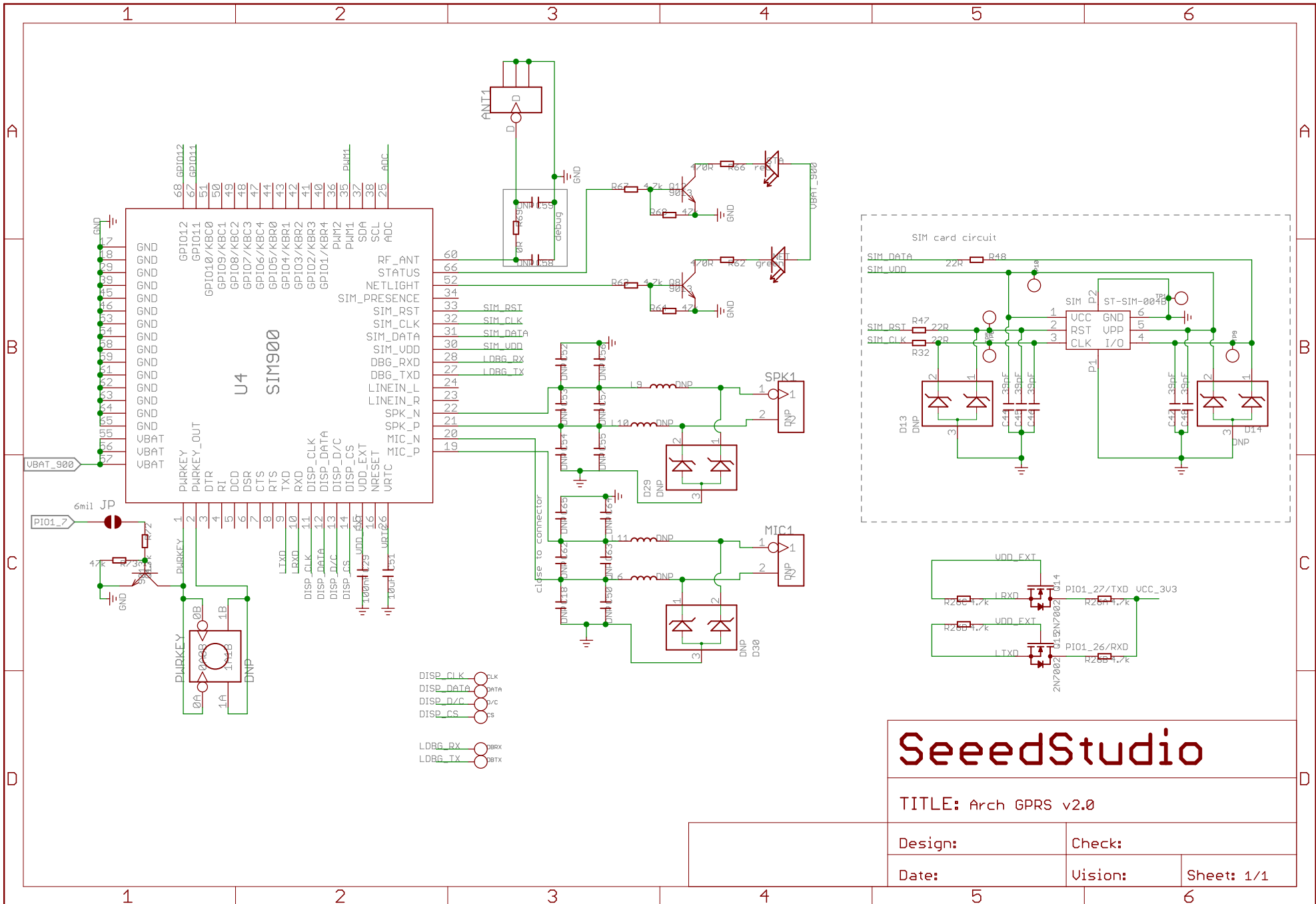


SeedStudio

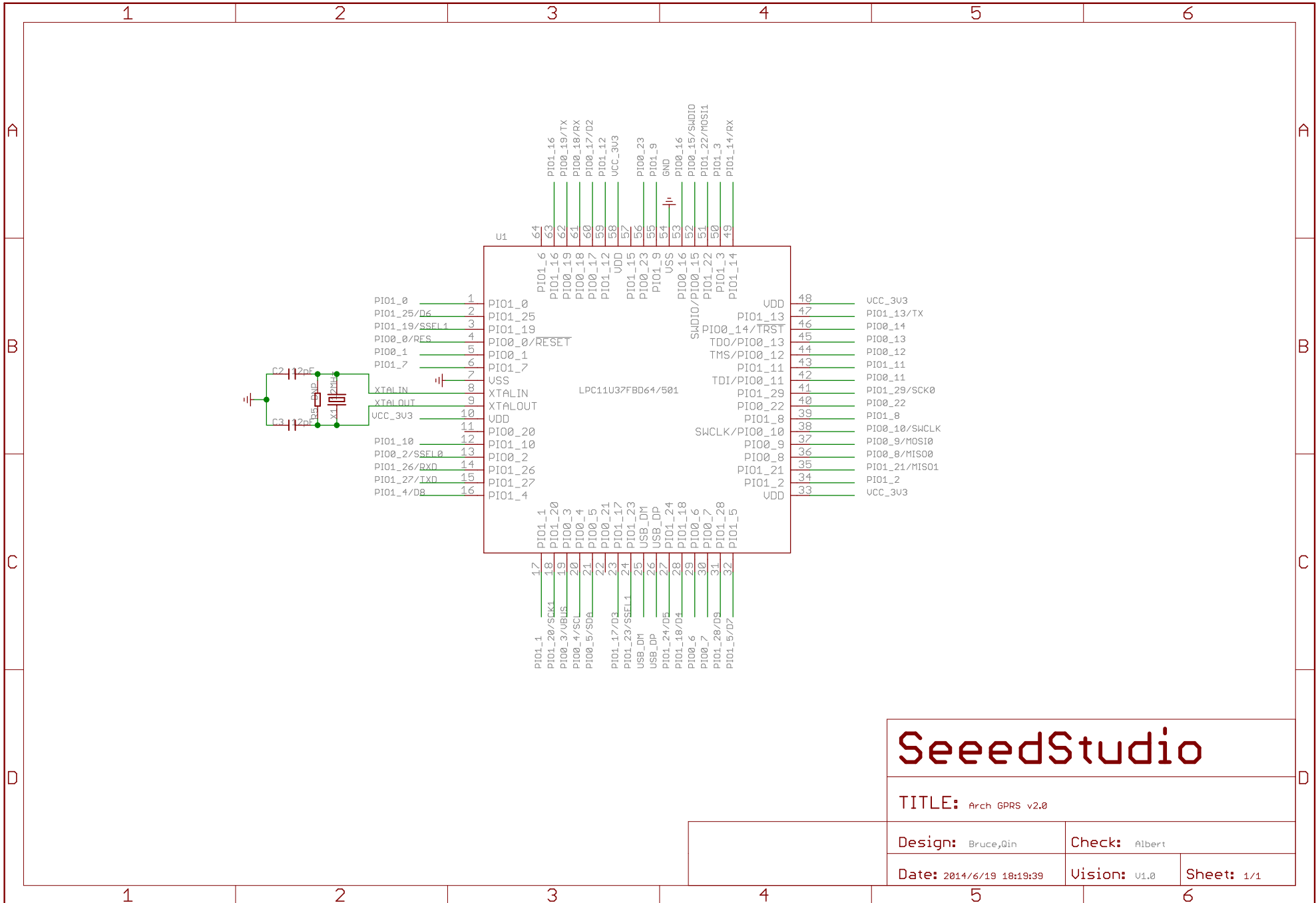
TITLE: Arch GPRS v2.0

Design: Bruce,Qin	Check: Albert
Date: 2014/6/19 18:19:39	Vision: V1.0
Sheet: 1/1	



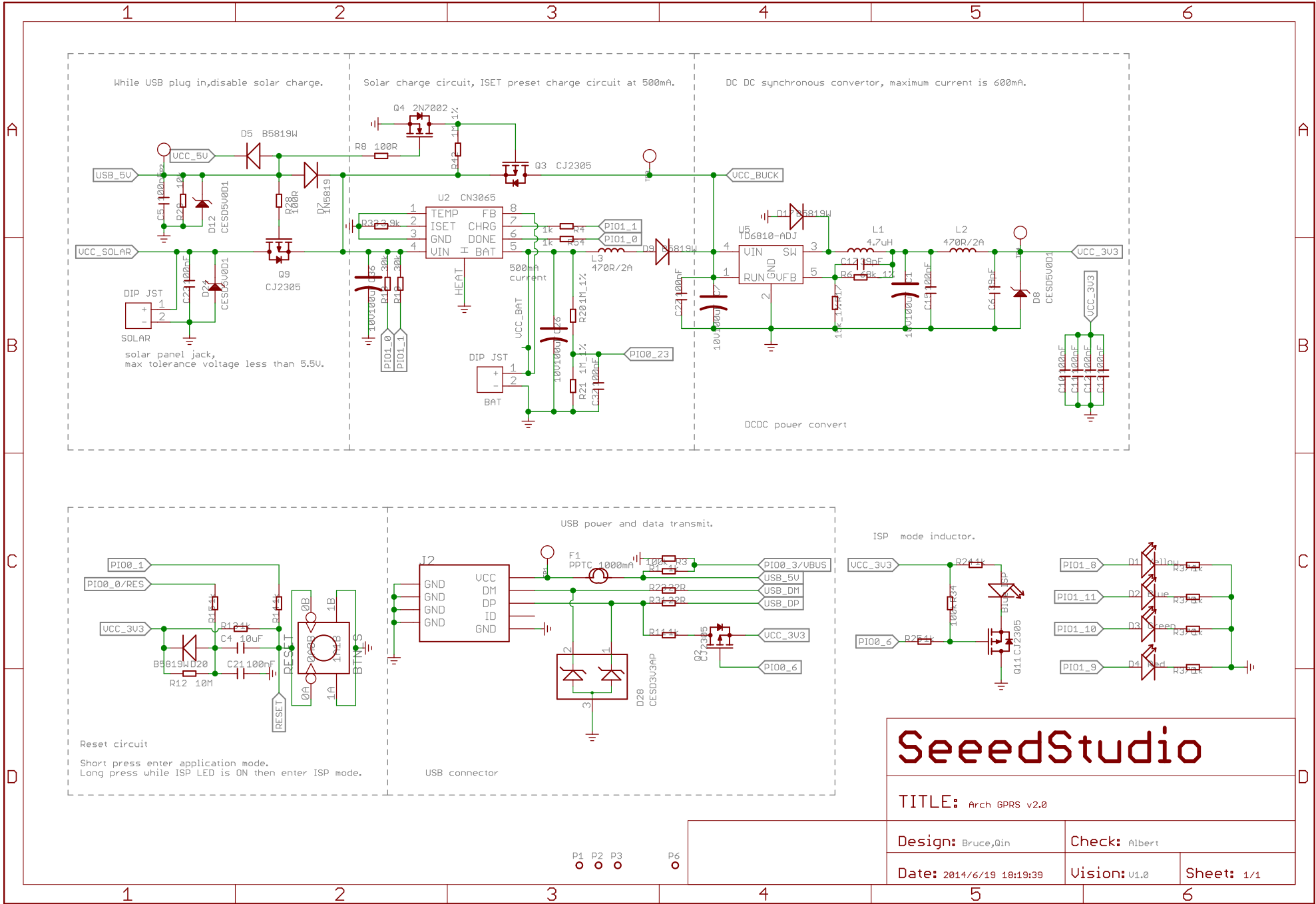
SeedStudio

TITLE: Arch GPRS v2.0		
Design:	Check:	
Date:	Vision:	Sheet: 1/1



SeeedStudio

TITLE: Arch GPRS v2.0		
Design: Bruce,Qin	Check: Albert	
Date: 2014/6/19 18:19:39	Vision: V1.0	Sheet: 1/1



1

2

3

4

5

6

While USB plug in,disable solar charge.

Solar charge circuit, ISET preset charge circuit at 500mA.

DC DC synchronous convertor, maximum current is 600mA.

UCC_5V0
 USB_5V
 UCC_SOLAR
 DIP JST
 SOLAR
 solar panel jack,
 max tolerance voltage less than 5.5V.

Q4 2N7002
 R8 100R
 Q3 CJ2305
 U2 CN3065
 TEMP FB
 ISET CHRG
 GND DONE
 VIN BAT
 HEAT
 UCC_BAT
 500mA
 current
 VCC_BAT
 DIP JST
 BAT

VCC_BUCK
 U5 TD6810-ADJ
 VIN SW
 RUN OUFB
 D1 B5819W
 L1 4.7uH
 L2 470R/2A
 C12 9pF
 B6 33k 1%
 C1 100nF
 C4 100nF
 C5 100nF
 D8
 UCC_3V3
 UCC_3V3

DCDC power convert

PI00_1
 PI00_0/RES
 UCC_3V3
 B5819W D20
 R12 10M
 RESET

Reset circuit
 Short press enter application mode.
 Long press while ISP LED is ON then enter ISP mode.

USB power and data transmit.
 I2
 UCC
 DM
 DP
 ID
 GND
 GND
 F1 PPTC 1000mA
 R3 100R
 R22 20R
 R33 20R
 R4 100R
 D28 CESD3V3NP
 R23 20R
 R24 100R
 PI00_3/UBUS
 USB_5V
 USB_DM
 USB_DP
 VCC_3V3
 PI00_6

USB connector

ISP mode inductor.
 UCC_3V3
 R24 100R
 R25 4k
 D11 CJ2305

PI01_8 D1 yellow R3 10k
 PI01_11 D2 blue R3 10k
 PI01_10 D3 green R3 10k
 PI01_9 D4 red R3 10k

SeeedStudio

TITLE: Arch GPRS v2.0

Design: Bruce,gin

Check: Albert

Date: 2014/6/19 18:19:39

Vision: V1.0

Sheet: 1/1



1

2

3

4

5

6